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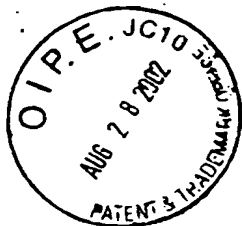
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2/20

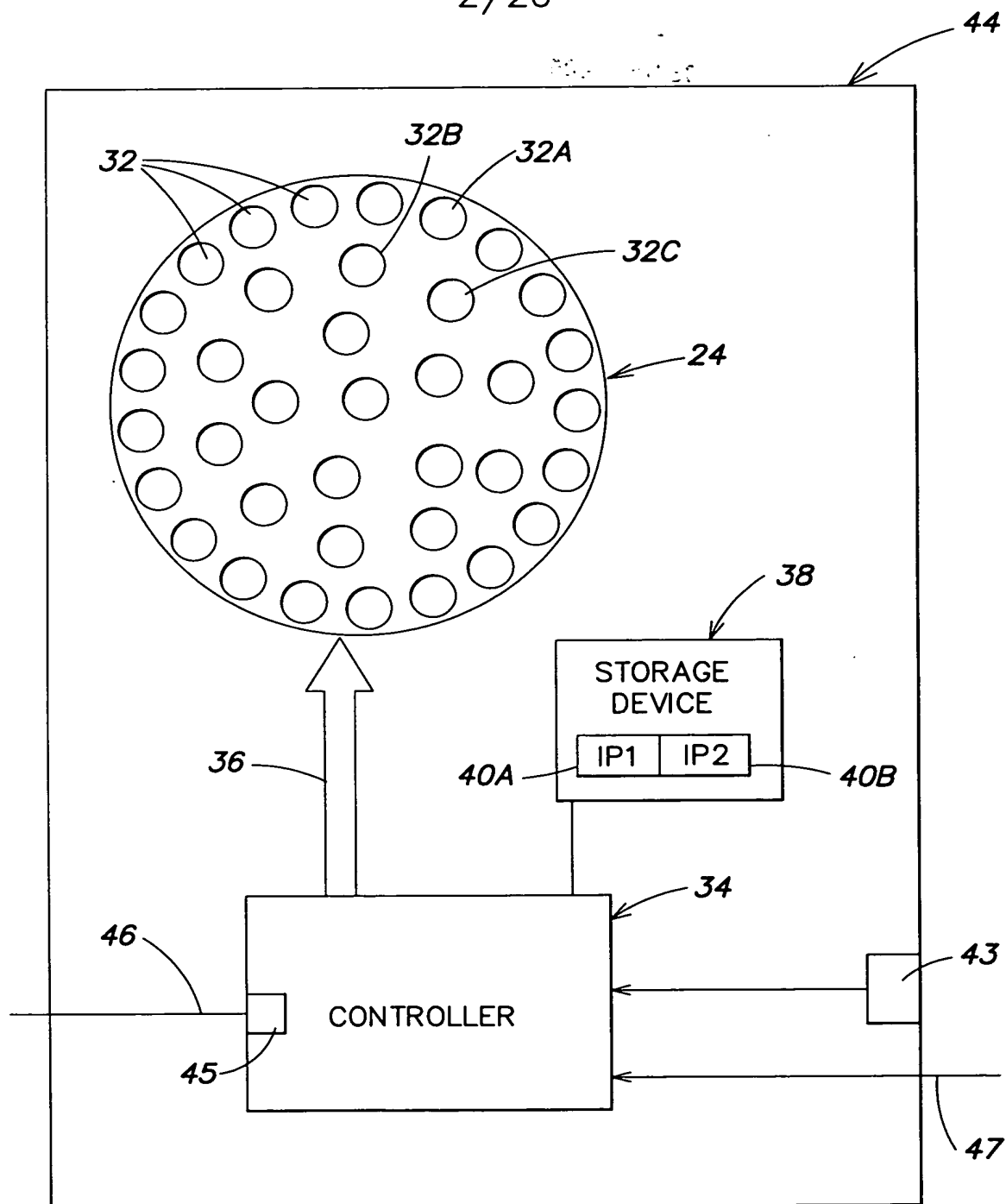
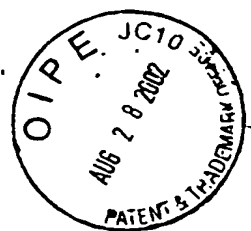


FIG. 2



3/20

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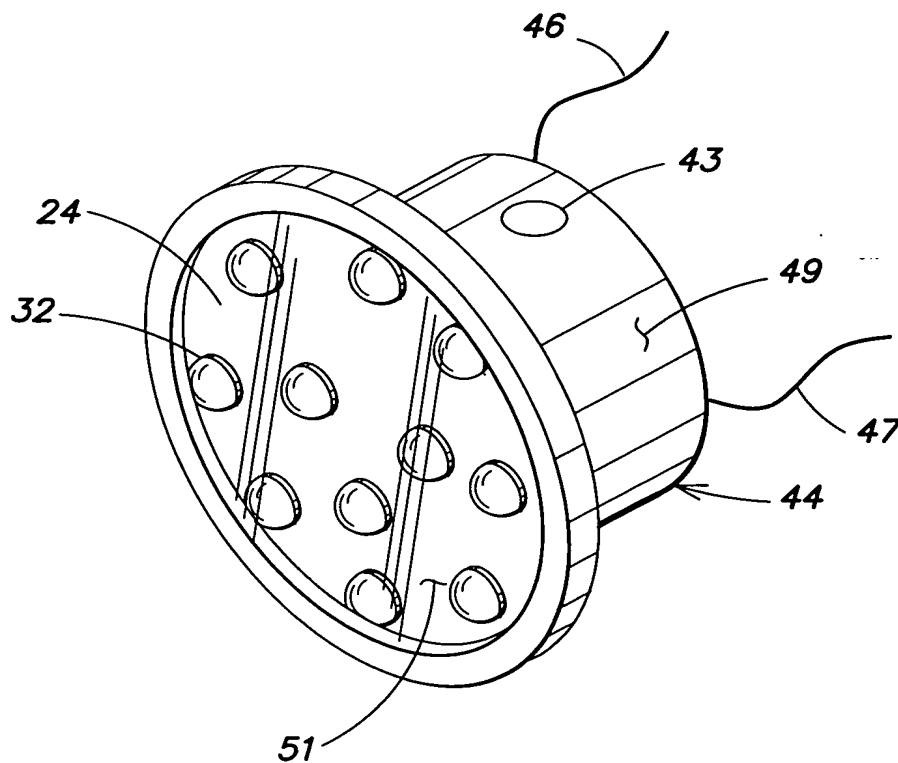
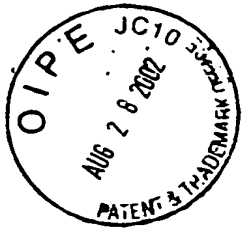


FIG. 3



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4/20

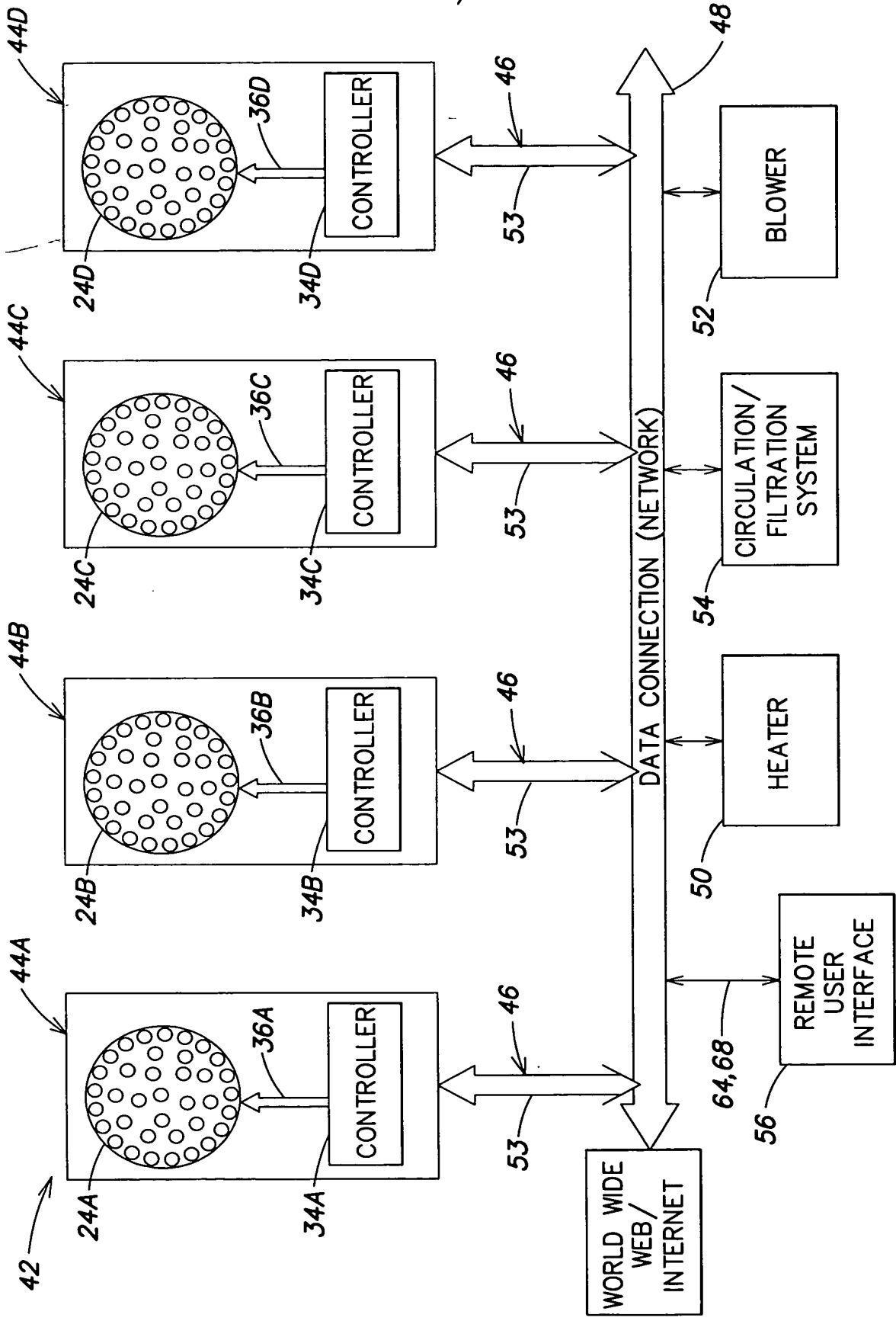
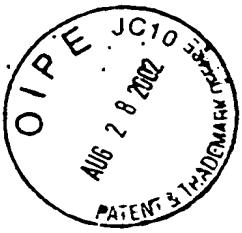


FIG. 4



5/20

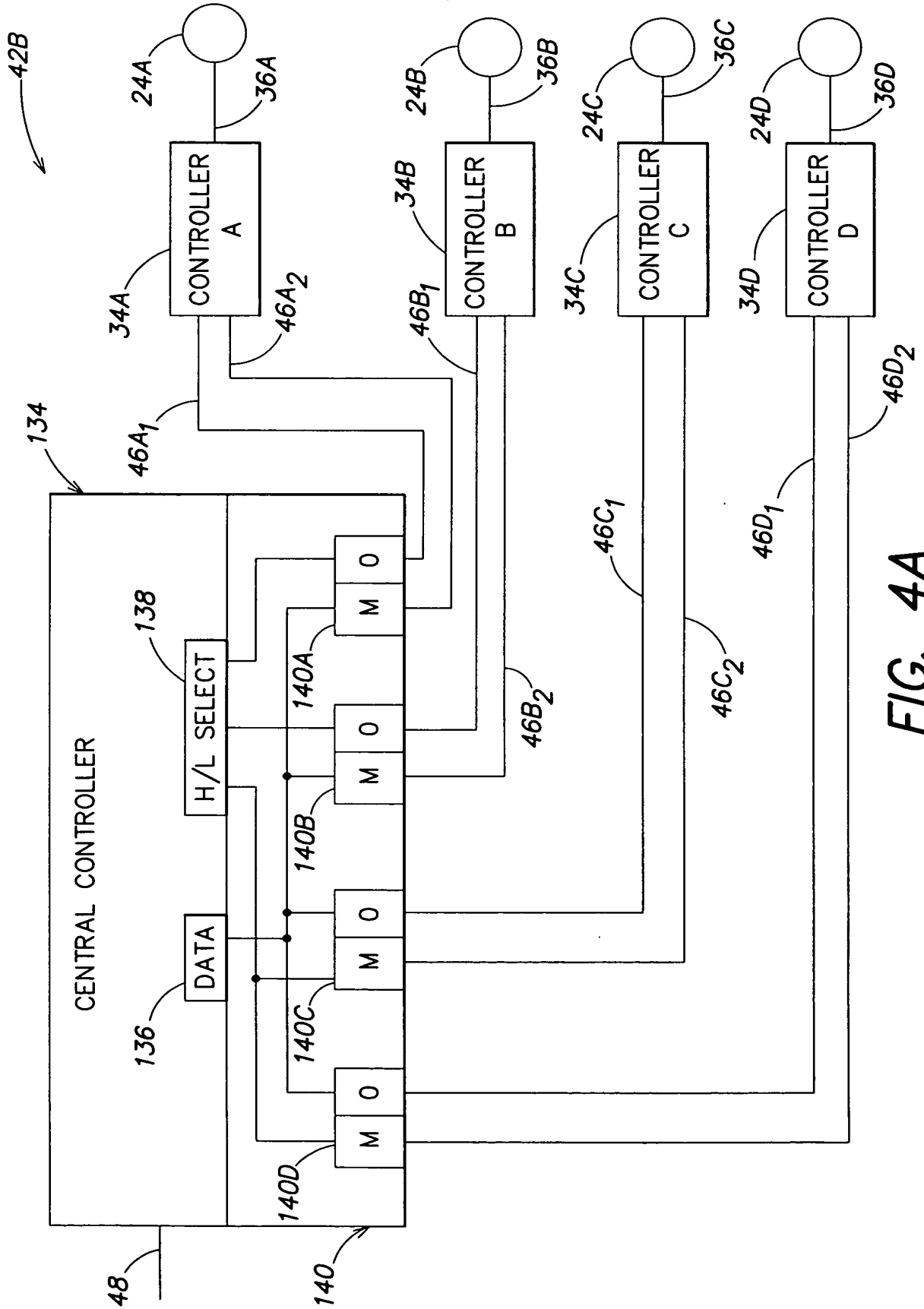
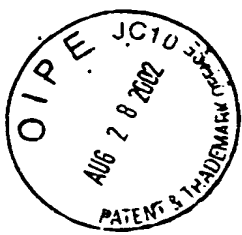


FIG. 4A

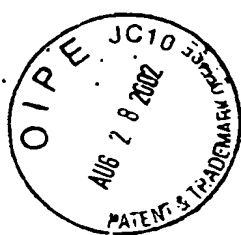


6/20

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B	DATA	LOGIC LOW
C	LOGIC HIGH	DATA
D	LOGIC LOW	DATA

*FIG. 4B*



7/20

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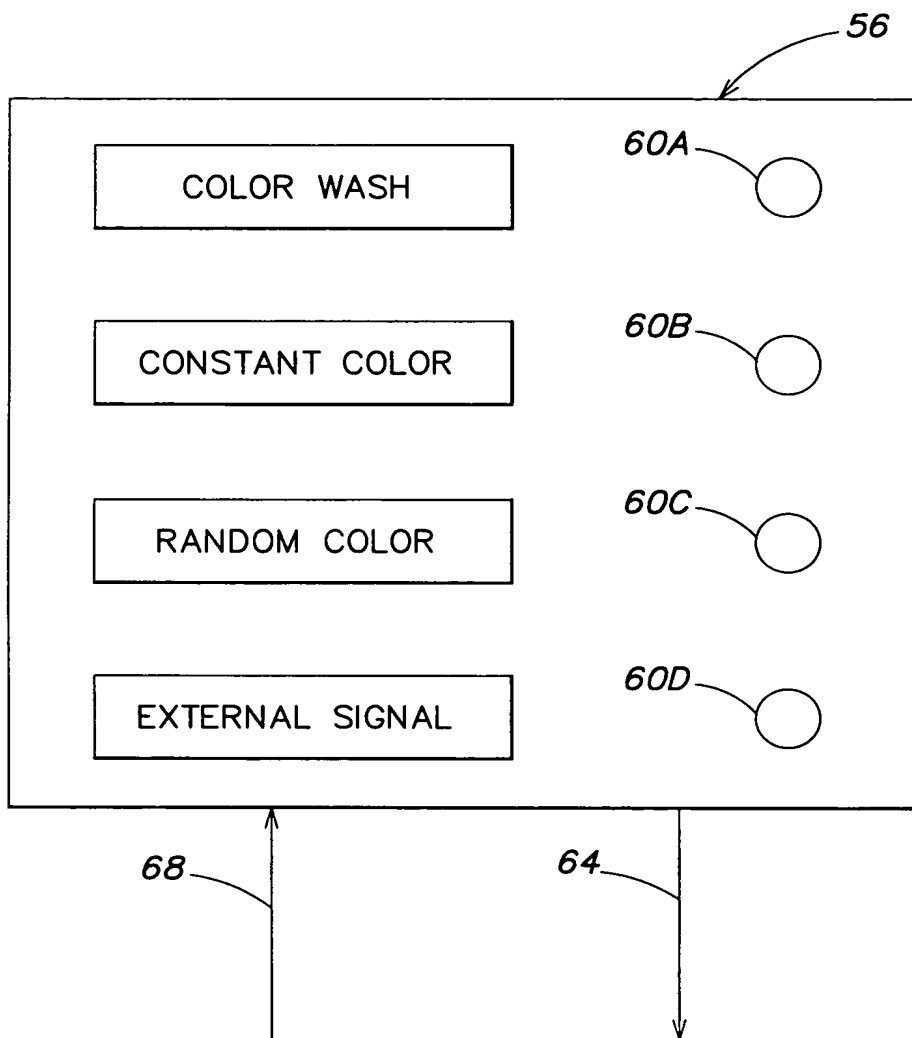
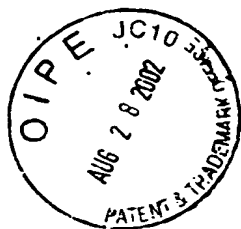


FIG. 5





8/20

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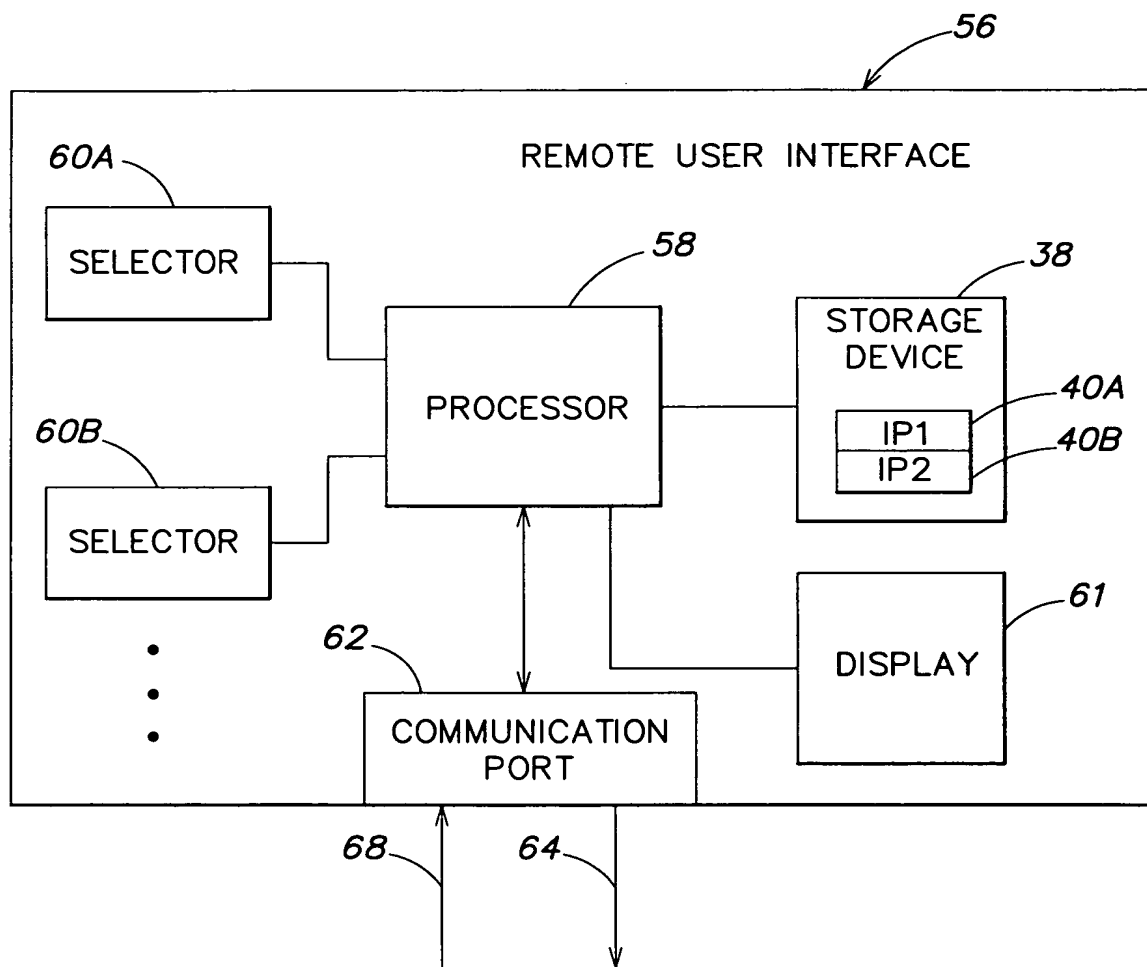
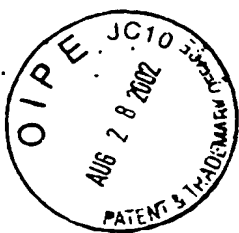


FIG. 6



9/20

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61

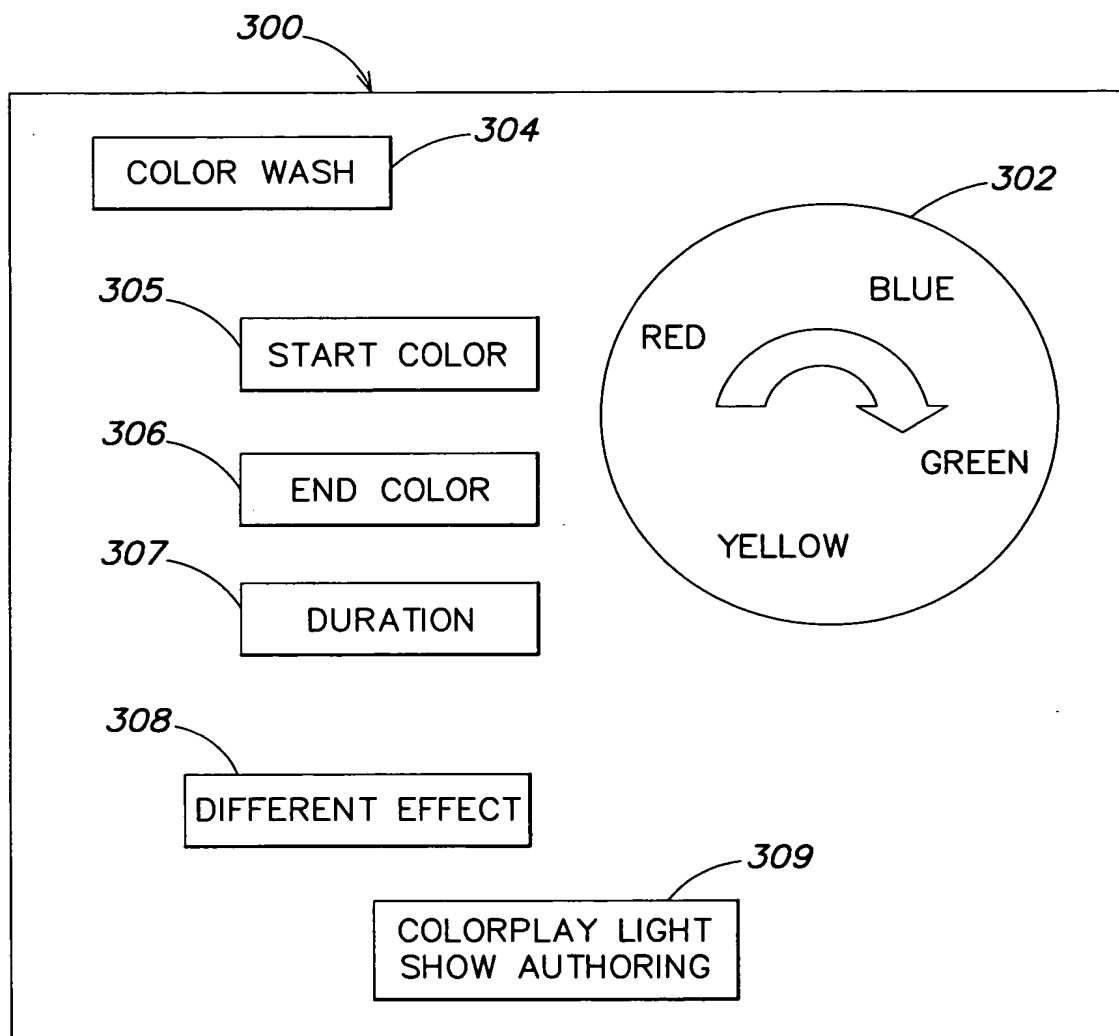
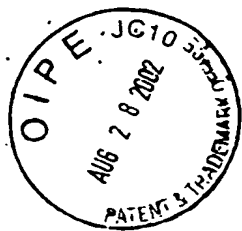


FIG. 7



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10/20

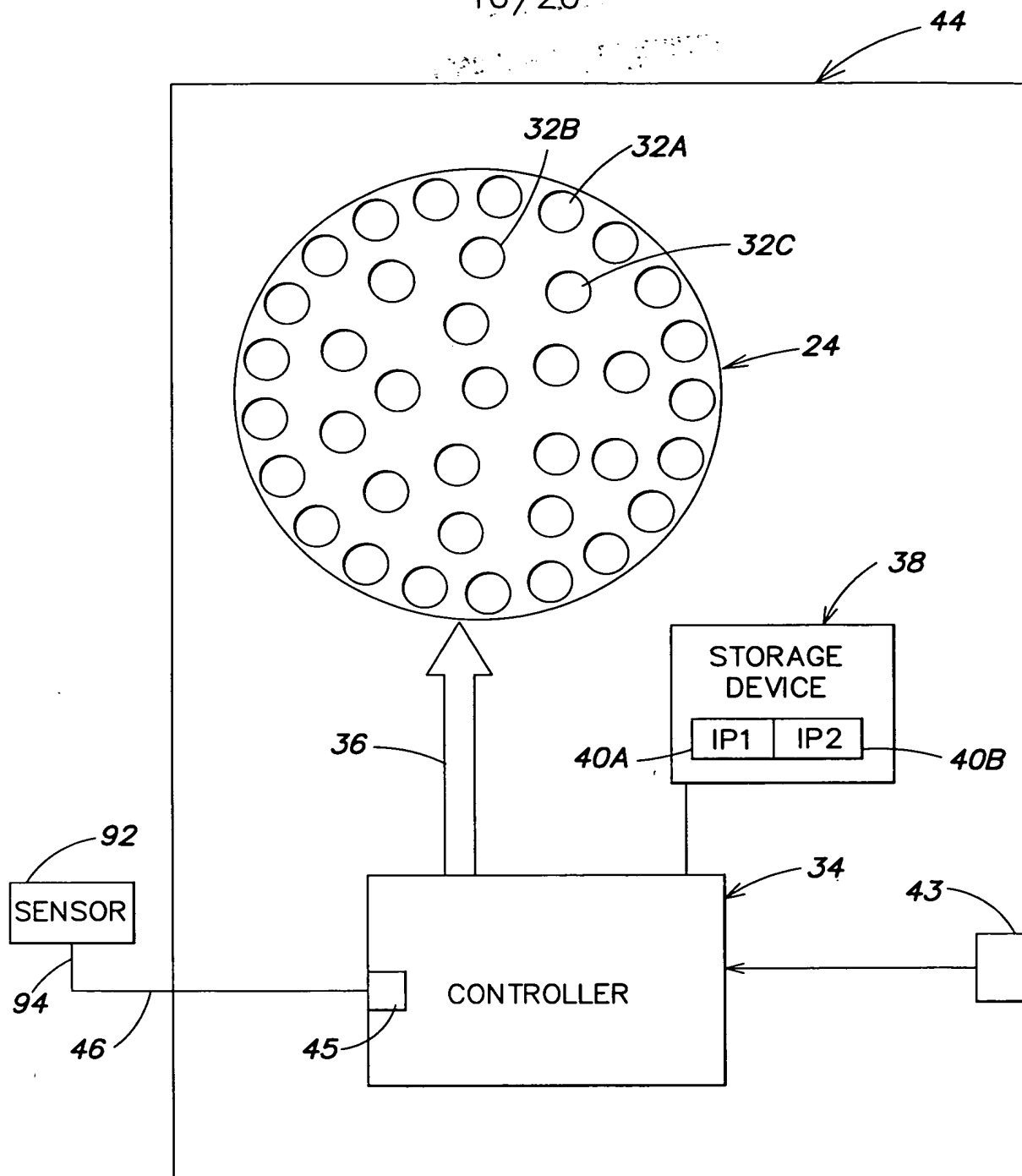
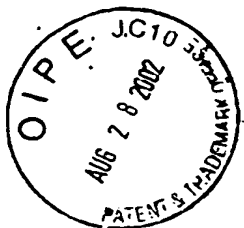


FIG. 8



11/20

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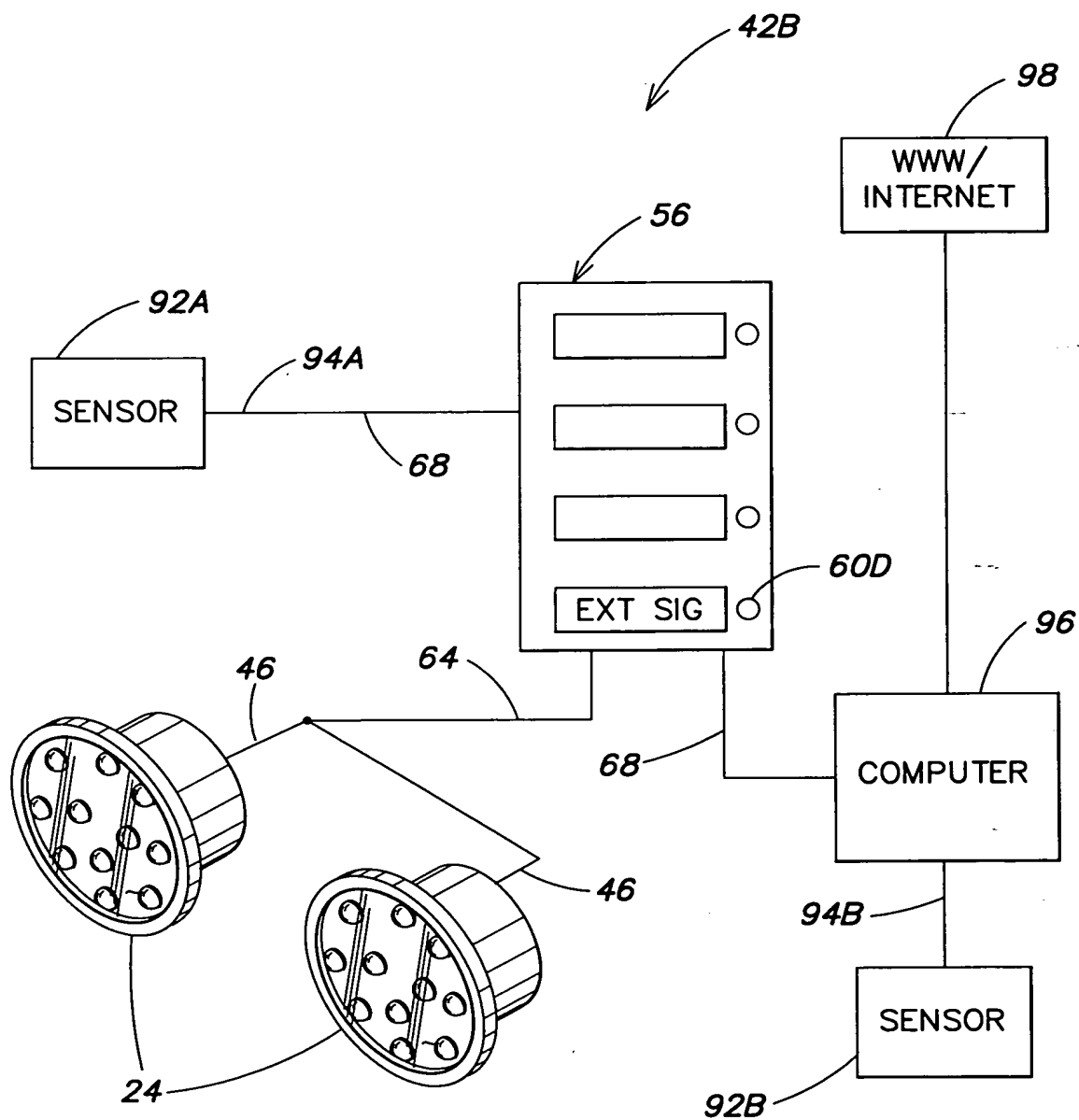
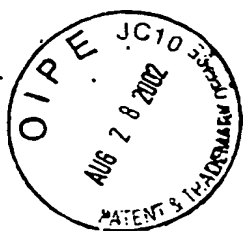


FIG. 9



12/20

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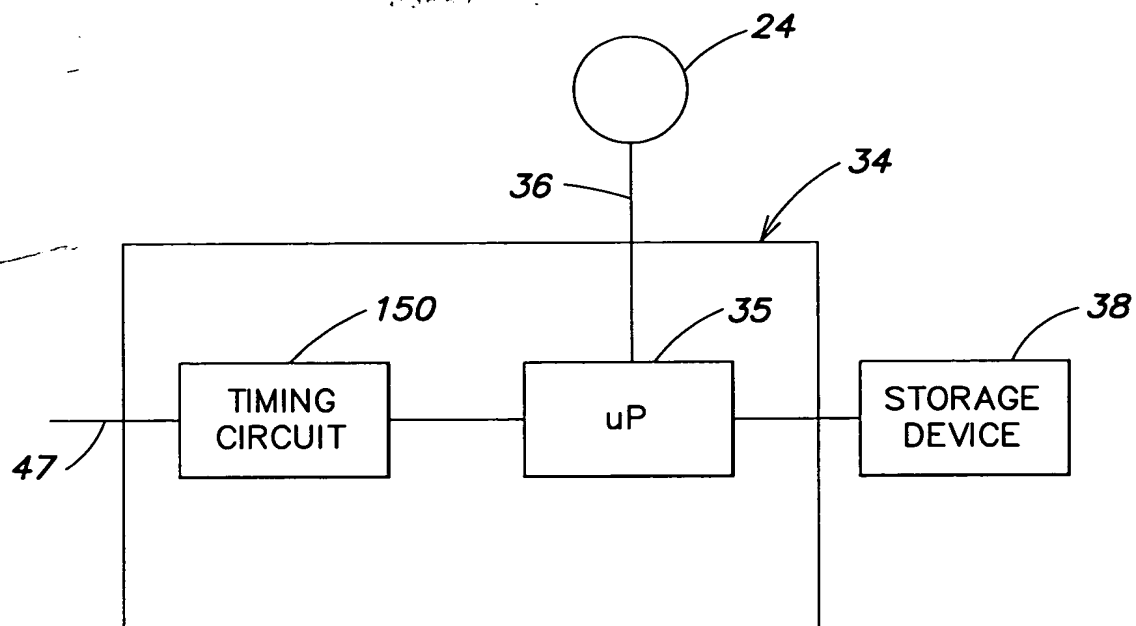
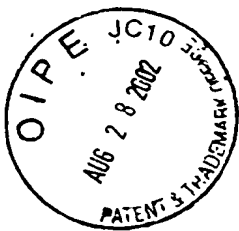


FIG. 10



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13/20

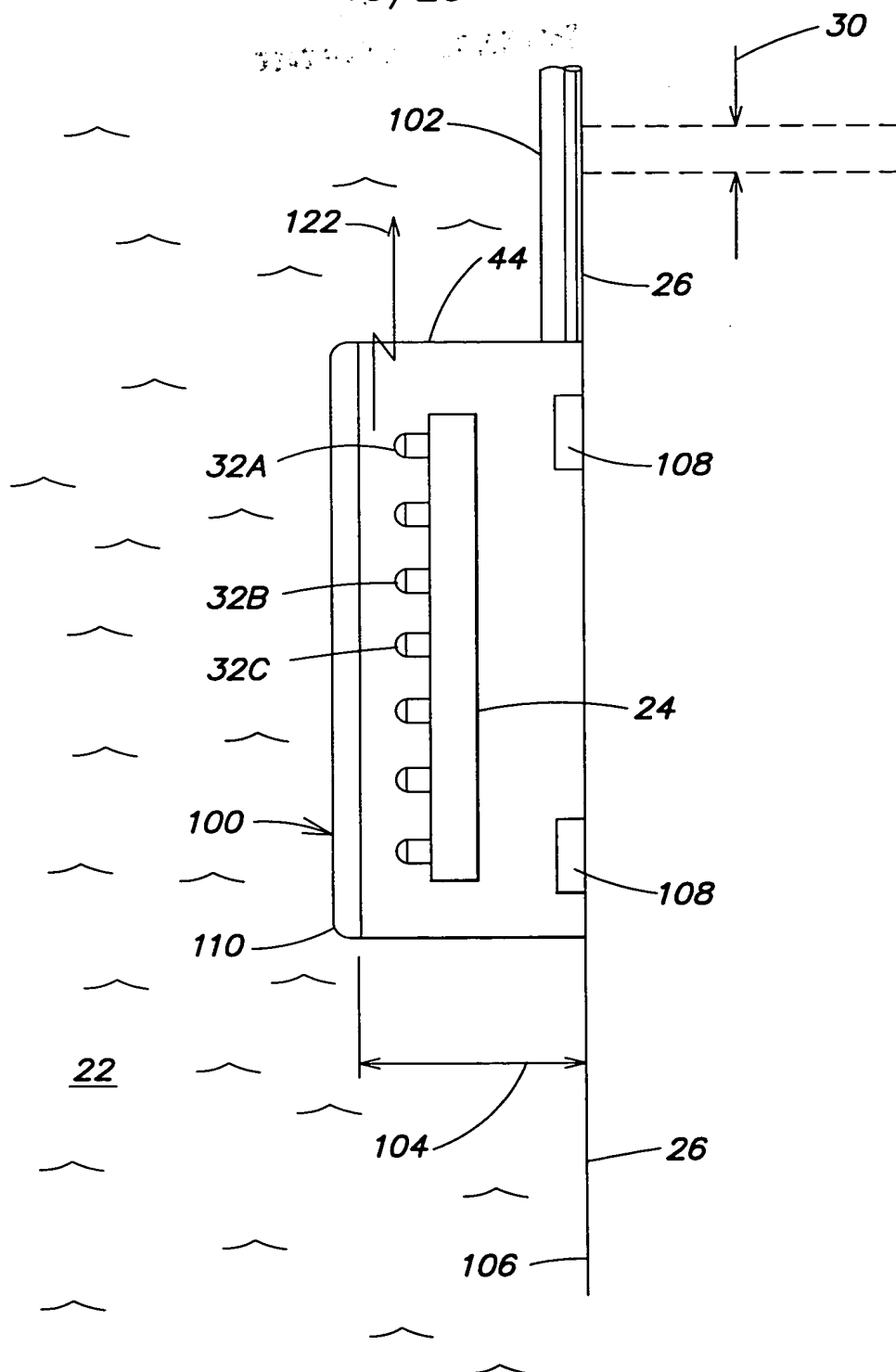
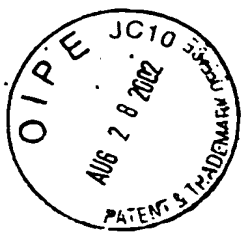


FIG. 11



14/20

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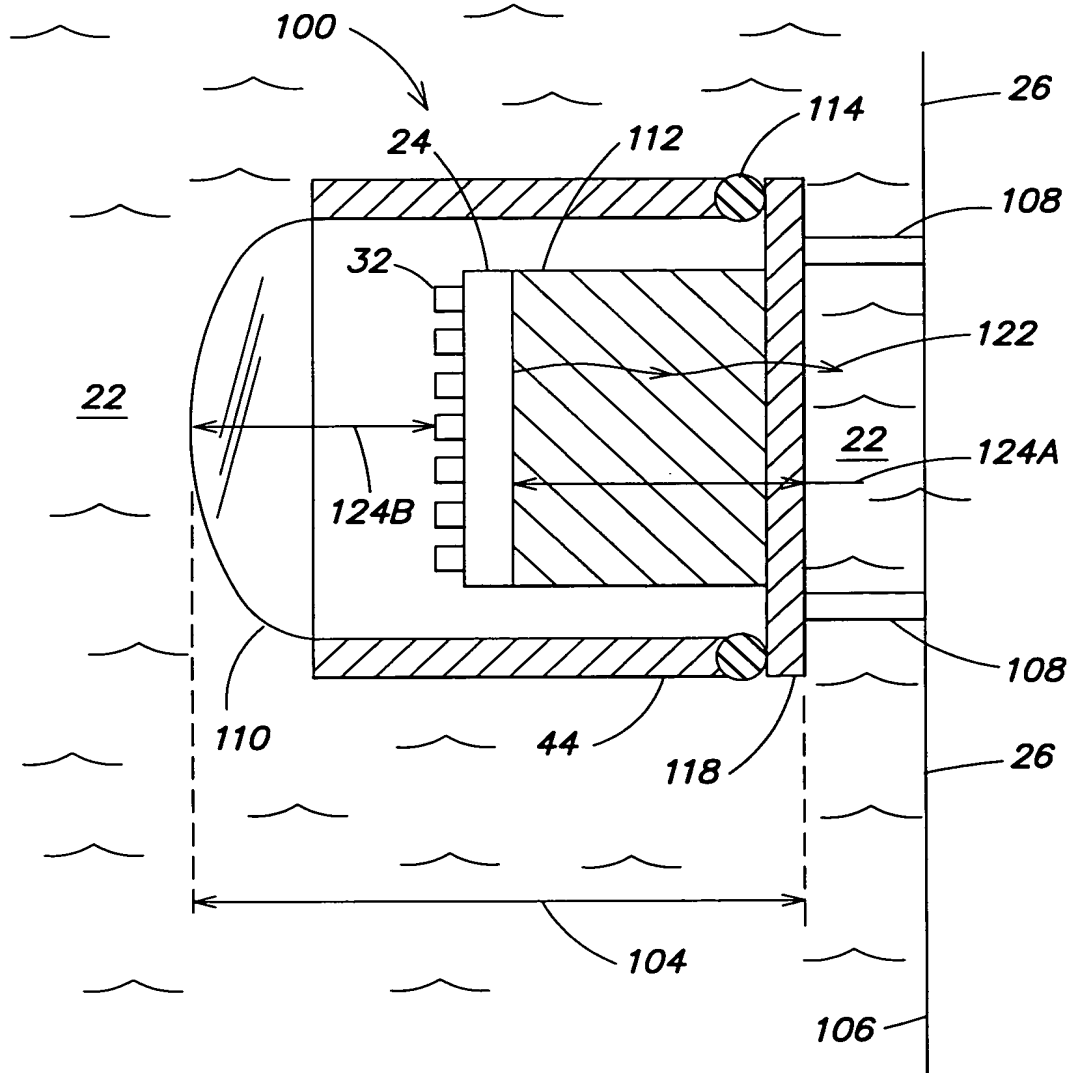
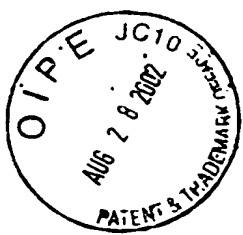
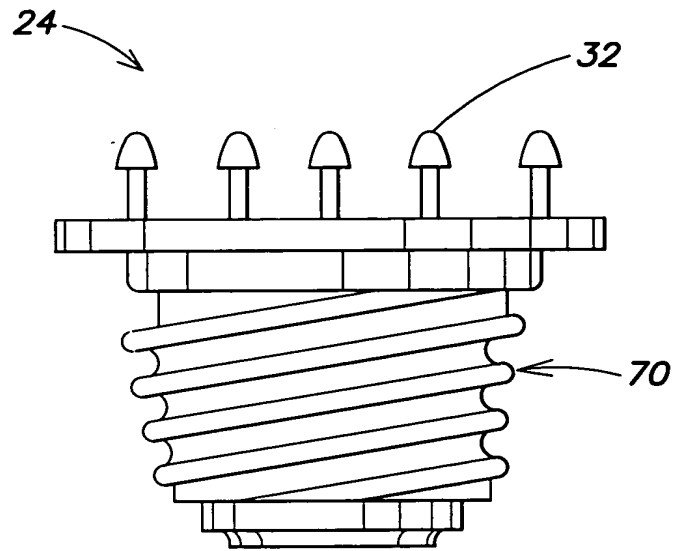


FIG. 12

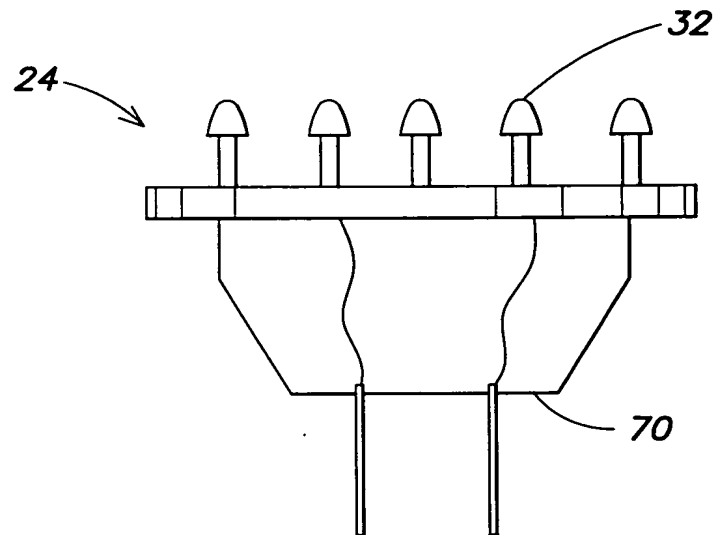


15/20

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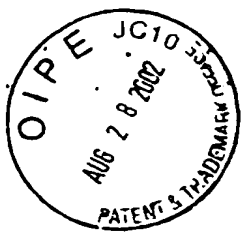


**FIG. 13**



**FIG. 14**





16/20

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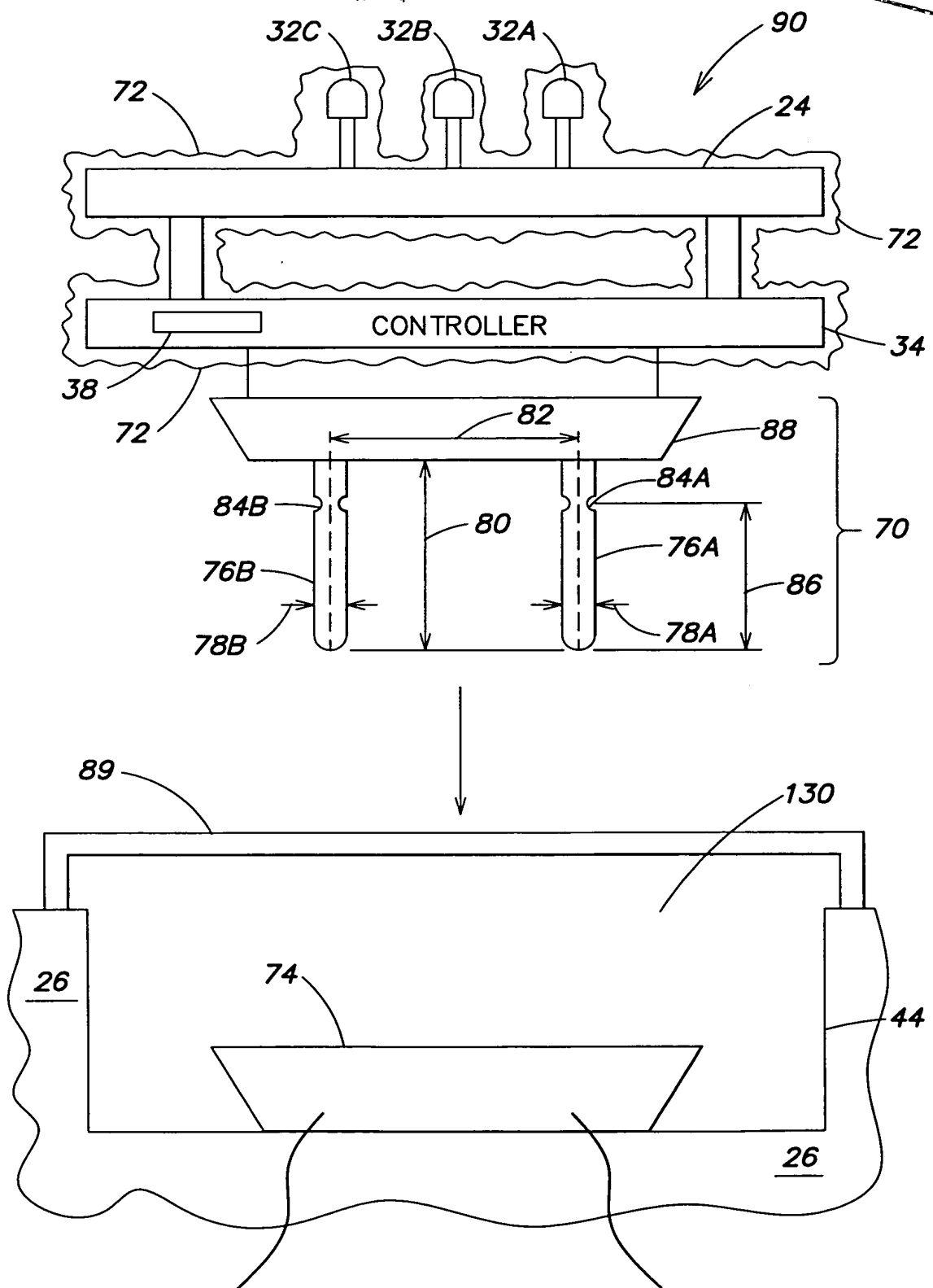
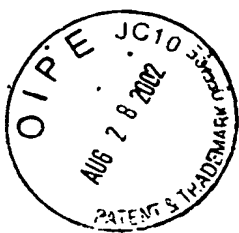
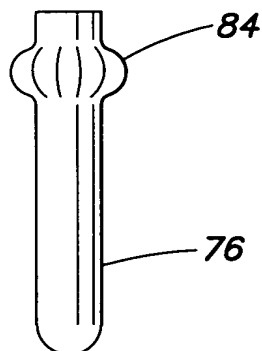


FIG. 15

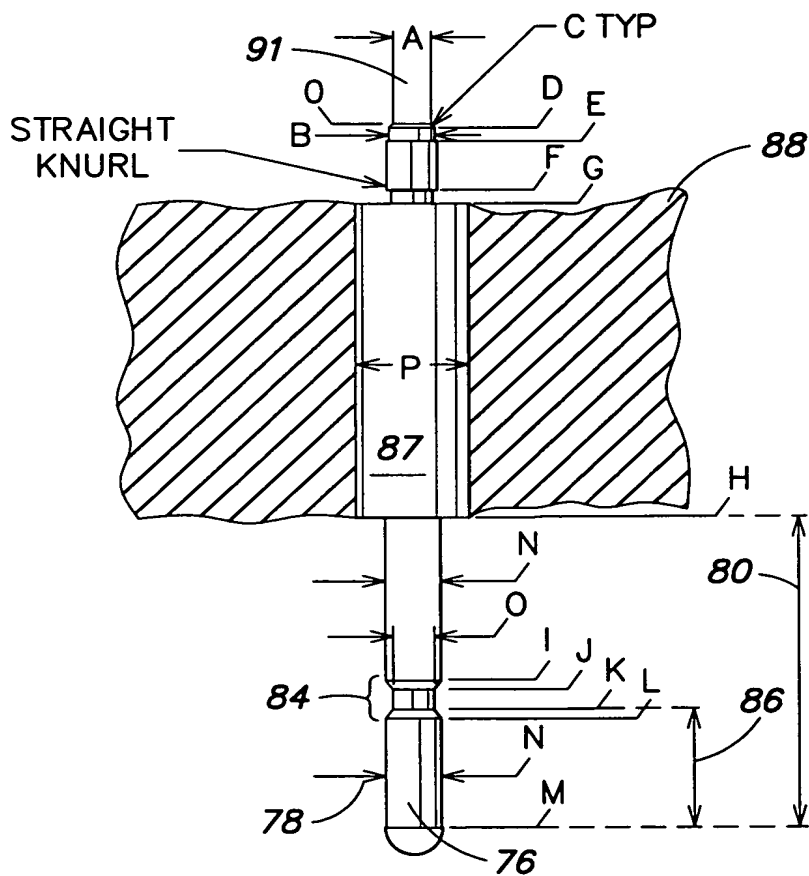


17/20

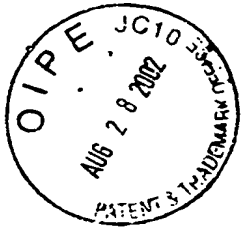
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**FIG. 16A**

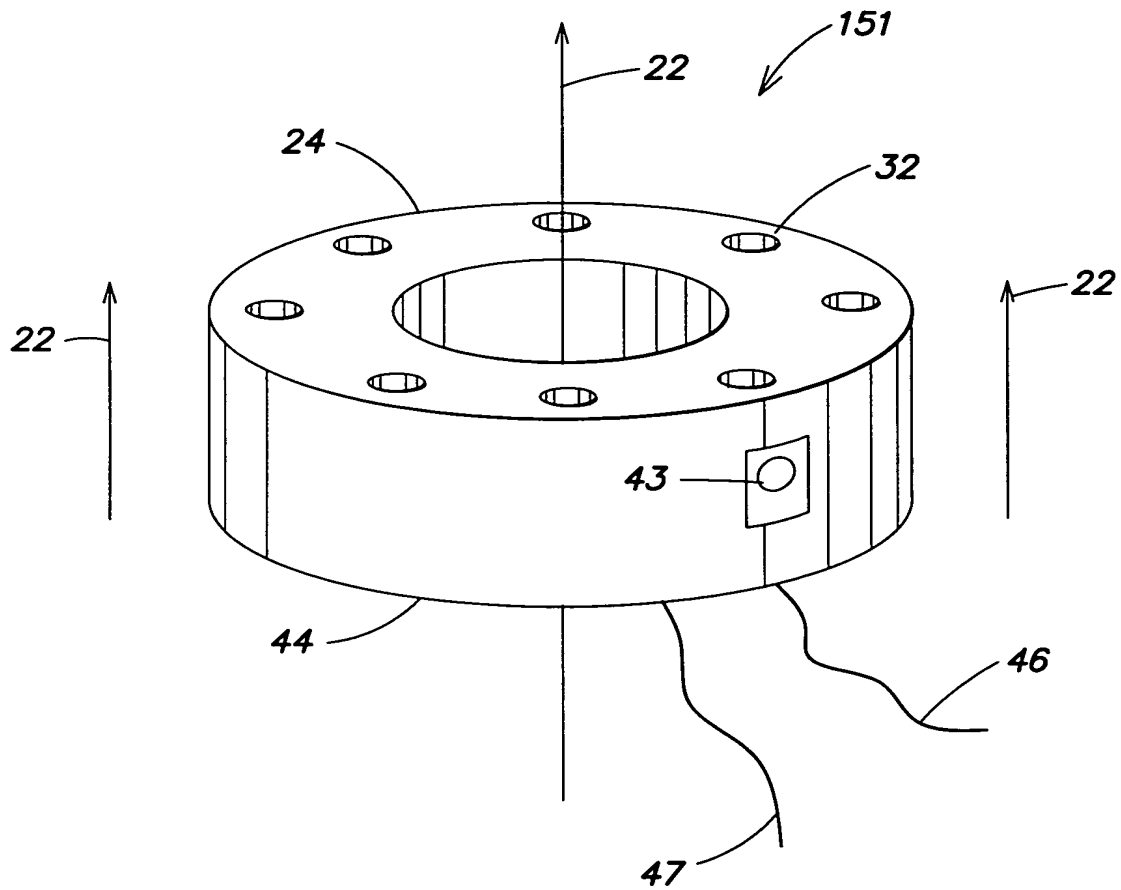


**FIG. 16B**

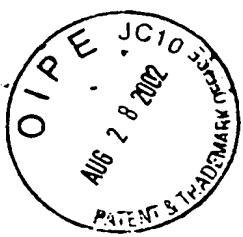


18/20

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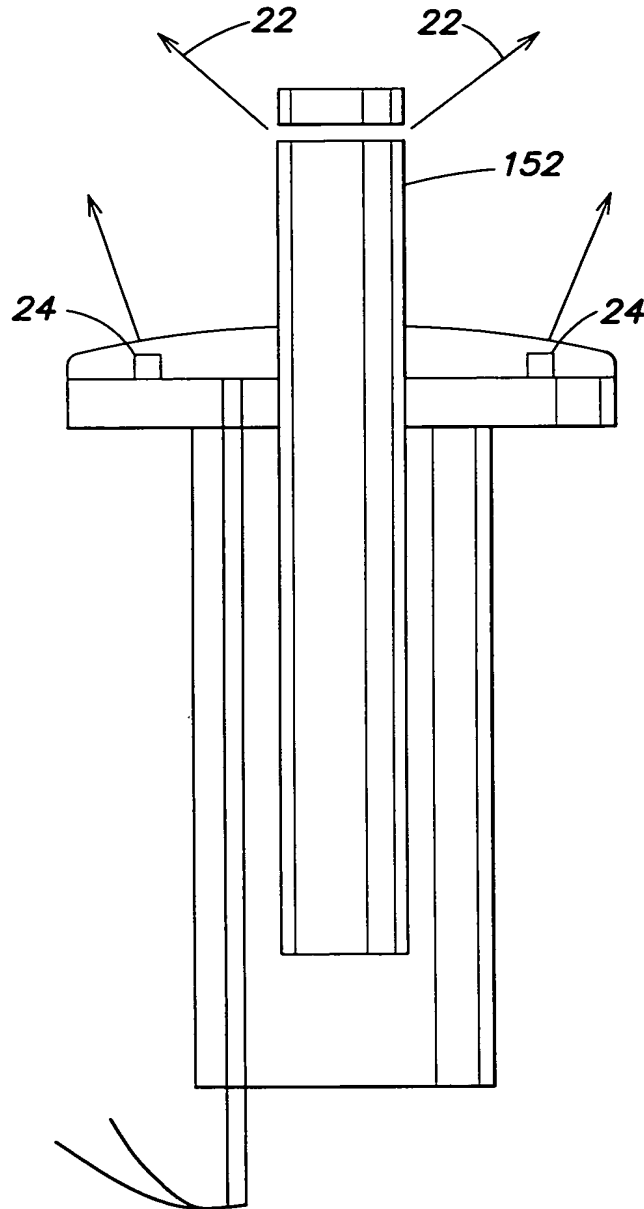
**FIG. 17**



10/19/2001

19/20

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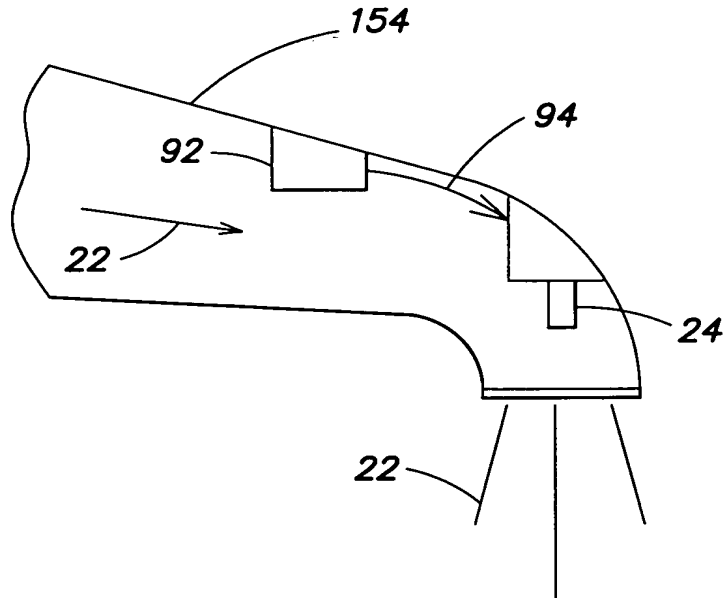


**FIG. 18**

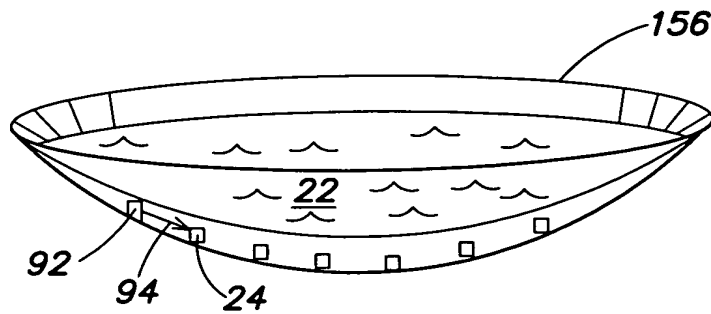


20/20

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**FIG. 19**



**FIG. 20**